University of Antwerp

Alloy - A lightweight object modelling notation

Maxim Gonnissen

https://groups.csail.mit.edu/sdg/pubs/2002/alloy-journal.pdf

Overview

- Functionality
- Language Definition
- Graphical Syntax
- Based on Z
- Tools
- Extensions
- Conclusion

“Alloy is a little language for describing structural properties”

https://groups.csail.mit.edu/sdg/pubs/2002/alloy-journal.pdf

Functionality

Functionality

- **Describing of structural properties**
- **Analysis**
  - Simulation (generation)
    - Detect overconstraint
  - Checking (counterexample)
    - Detect underconstraint

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Language Definition

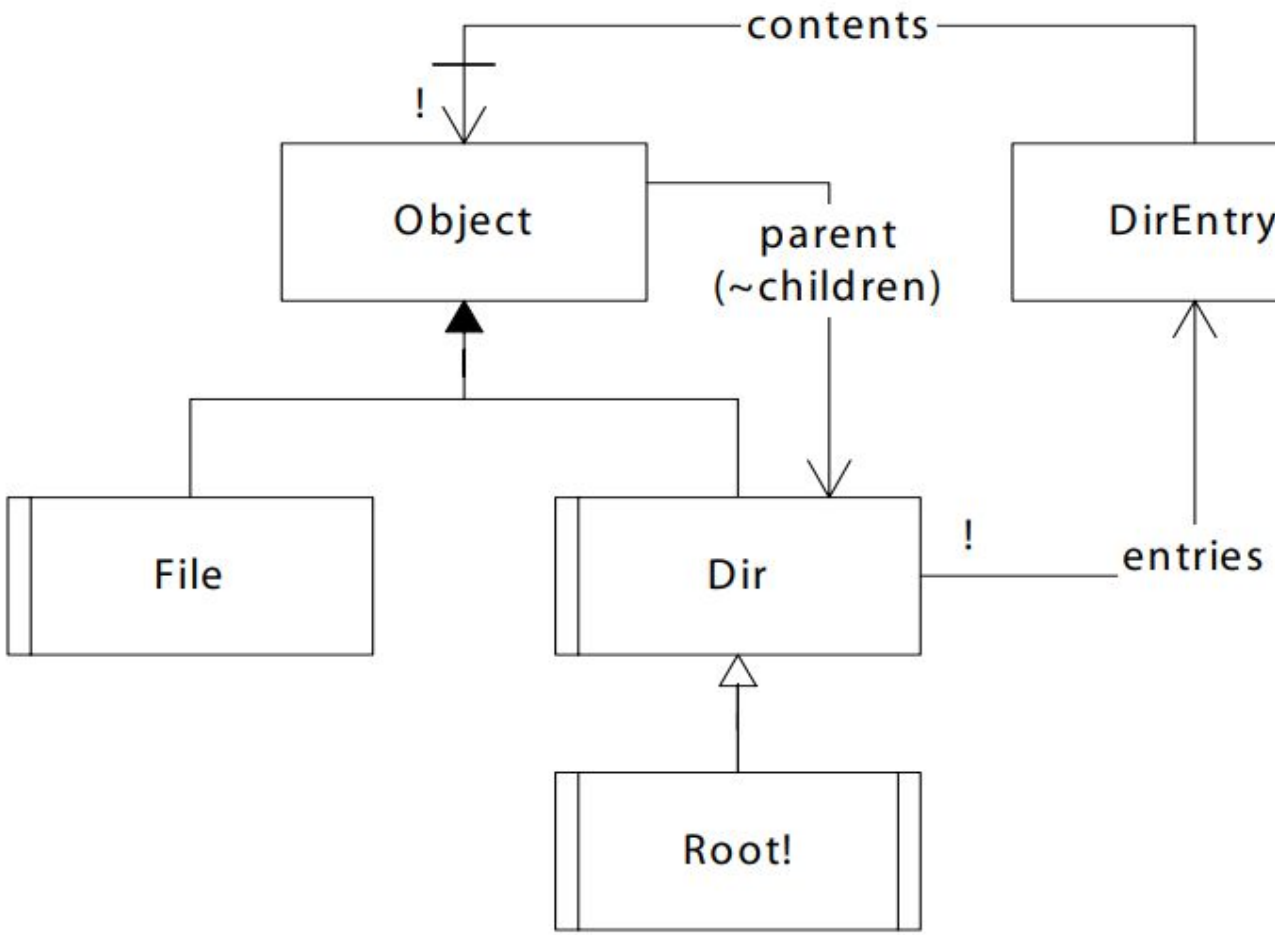
Language Definition

- **Strongly-typed kernel**
- **Singleton-set scalars**
- **Degenerate relation set encoding**
- **Dot operator**
- **Full language**
  - Declarations
  - Shorthands
  - Paragraphs

Graphical Syntax

Graphical Syntax

- State
- Constraints
- Comparison to UML




```
model FileSystem {
    domain {Object, DirEntry, fixed Name}
    state {
        partition File, Dir : static Object
        Root: fixed Dir!
        entries: Dir! -> DirEntry
        name: DirEntry -> static Name!
        contents: DirEntry -> static Object!
        parent (~children) : Object -> Dir
        }
    def parent { all o | o.parent = o.~contents.~entries }
    inv UniqueNames {all d | all e1, e2: d.entries | e1.name = e2.name -> e1 = e2}
    inv Parents {
        no Root.parent
        all d: Dir - Root | one d.parent }
    inv Acyclic {no d | d in d.+parent}
    inv Reachable {Object in Root.*children}
    cond TwoDeep {some Root.children.children}
    assert FileHasEntry {all f: File | some d | f in d.entries.contents}
    assert AtMostOneParent {all o | sole o.parent}
    op NewDirEntries (d: Dir, es: DirEntry') {
        no es & DirEntry
        d.entries' = d.entries + es
        all x: Dir - d | x.entries' = x.entries }
    op Create (d: Dir!, o: Object'!, n: Name) {
        n !in d.entries.name
        some e: DirEntry' | NewDirEntries (d, e) && e.contents' = o && e.name' = n }
    assert EntriesCreated {all d: Dir, e: DirEntry' | NewDirEntries (d,e) -> DirEntry' = DirEntry + e}
    assert CreateWorks {all d, o, n | Create (d,o,n) -> o in d.children'}
    }
```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Based on Z

Based on Z

- **Why Z?**
  - Why not OCL?
- **Automatic Analysis**
- **Set-Based Syntax**
- **Mutability**
- **Lexical Issues**

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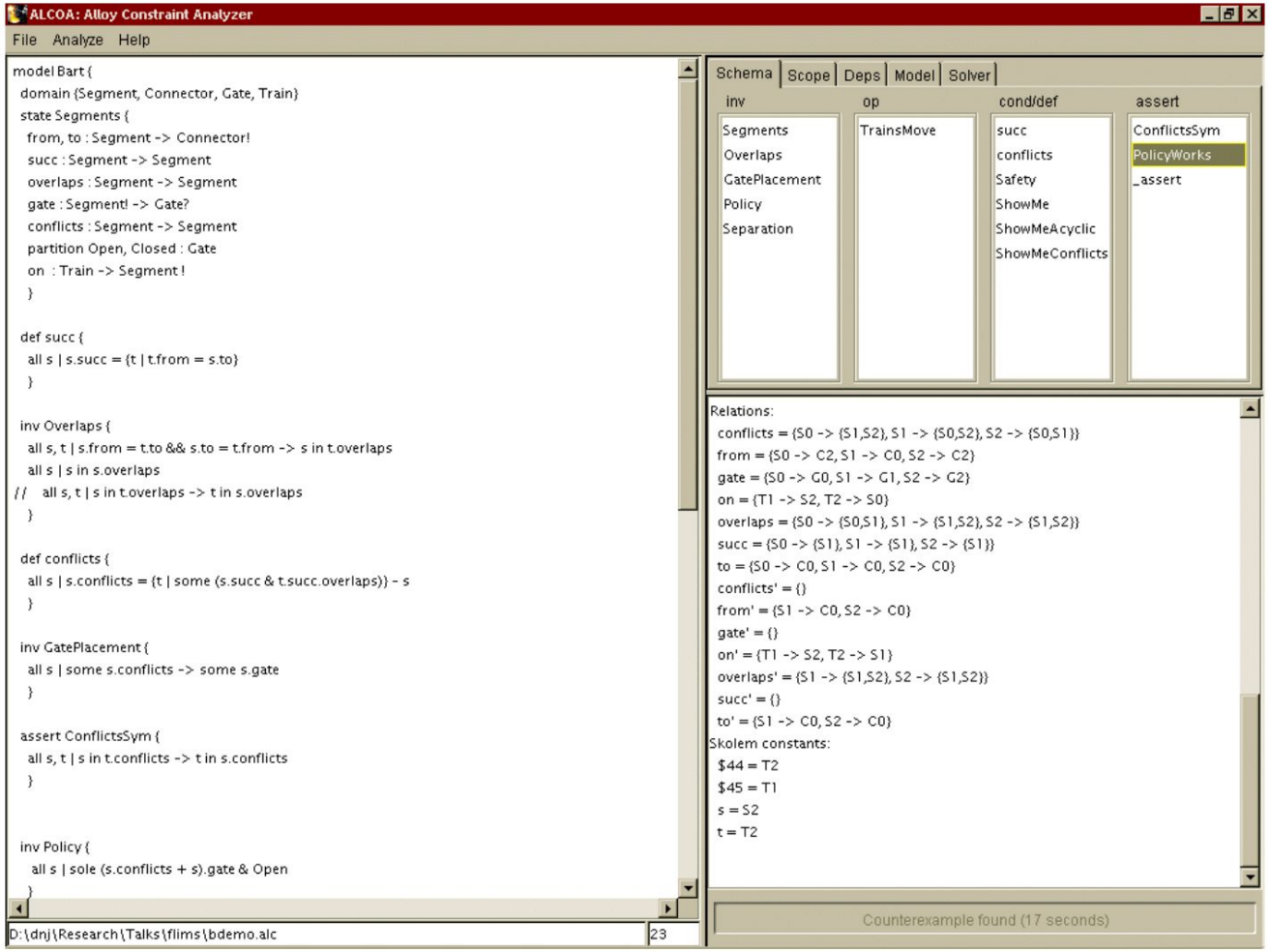
Tools

Tools

- Alcoa: The Alloy Constraint Analyzer
- Alloy Analyzer

Alcoa: The Alloy Constraint Analyzer



- Nitpick
- SAT Solver
- Analysis
  - Checks
  - Generation
- Finite scope

Alloy Analyzer

File Edit Execute Options Window Help

New Open Reload Save Execute Show

```
// A file system object in the file system
sig FSObject { parent: lone Dir }

// A directory in the file system
sig Dir extends FSObject { contents: set FSObject }

// A file in the file system
sig File extends FSObject { }

// A directory is the parent of its contents
fact { all d: Dir, o: d.contents | o.parent = d }

// All file system objects are either files or directories
fact { File + Dir = FSObject }

// There exists a root
one sig Root extends Dir { } { no parent }

// File system is connected
fact { FSObject in Root.*contents }
```

Executing "Run run$1"
Solver=sat4j Bitwidth=4 MaxSeq=
326 vars. 36 primary vars. 519 cl
Instance found. Predicate is con

Executing "Run run$1"
Solver=sat4j Bitwidth=4 MaxSeq=
330 vars. 36 primary vars. 523 cl
Instance found. Predicate is con

Executing "Check acyclic for 5"
Solver=sat4j Bitwidth=4 MaxSeq=
1124 vars. 67 primary vars. 1915
No counterexample found. Asser

Executing "Run Default for 4 but
Solver=sat4j Bitwidth=4 MaxSeq=
403 vars. 41 primary vars. 516 cl
Instance found. Predicate is con

Executing "Run run$1"
Solver=sat4j Bitwidth=4 MaxSeq=
330 vars. 36 primary vars. 514 cl
Instance found. Predicate is con

Executing "Run run$1"
Solver=sat4j Bitwidth=4 MaxSeq=
326 vars. 36 primary vars. 510 cl
Instance found. Predicate is con

Executing "Run run$1"
Solver=sat4j Bitwidth=4 MaxSeq=
326 vars. 36 primary vars. 510 cl
Instance found. Predicate is con

Executing "Run Default for 4 but
Solver=sat4j Bitwidth=4 MaxSeq=
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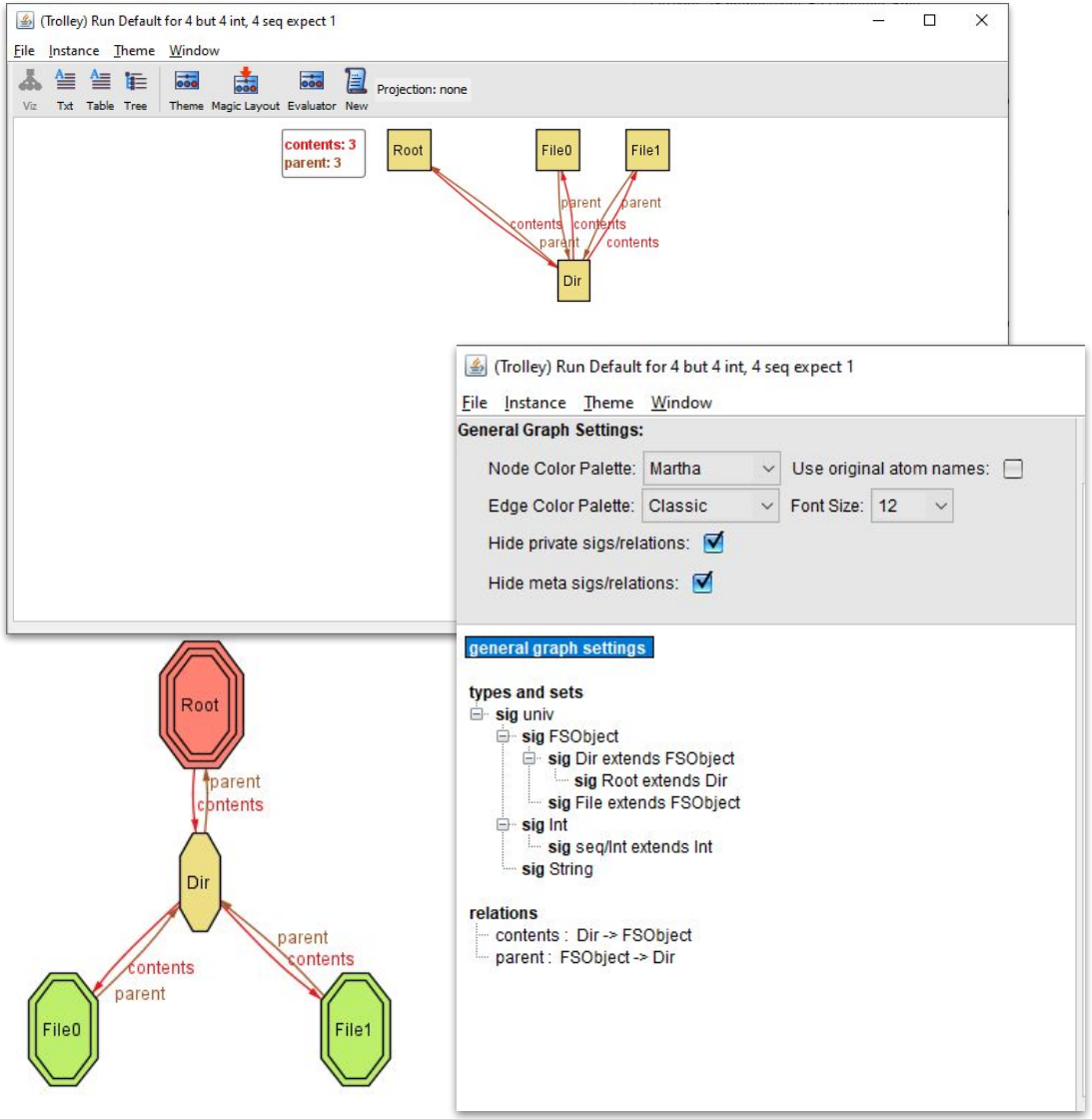
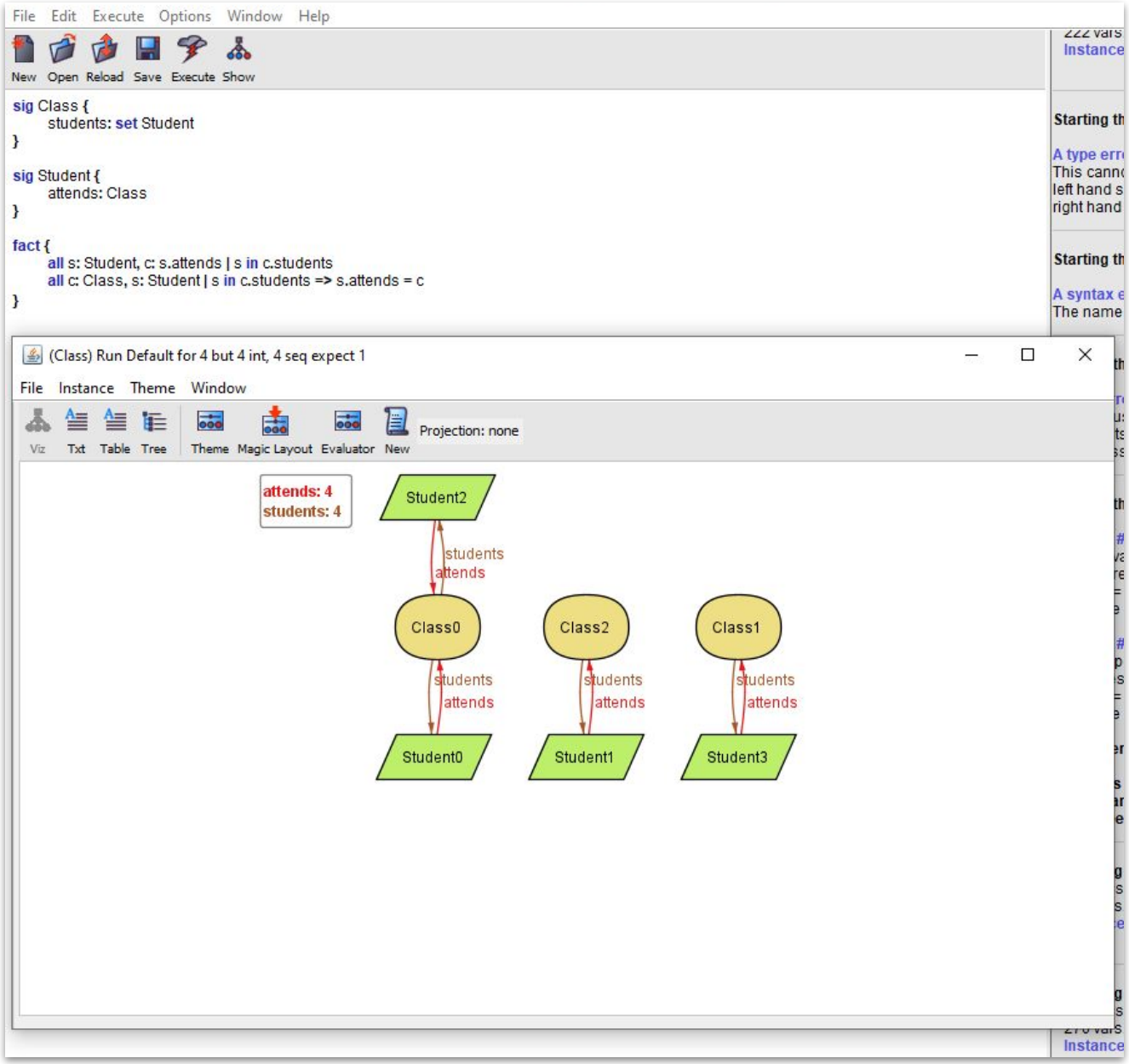
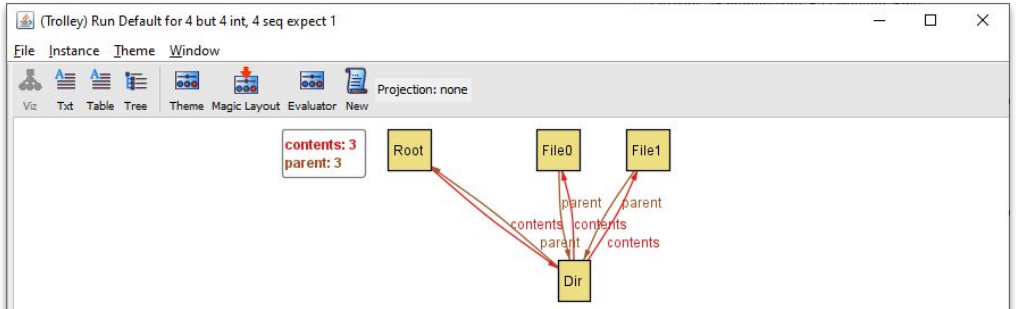
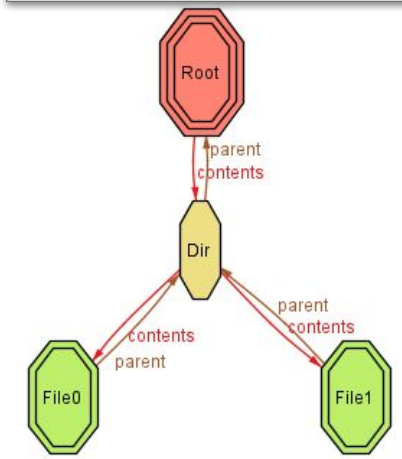
Line 20, Column 36 [modified]

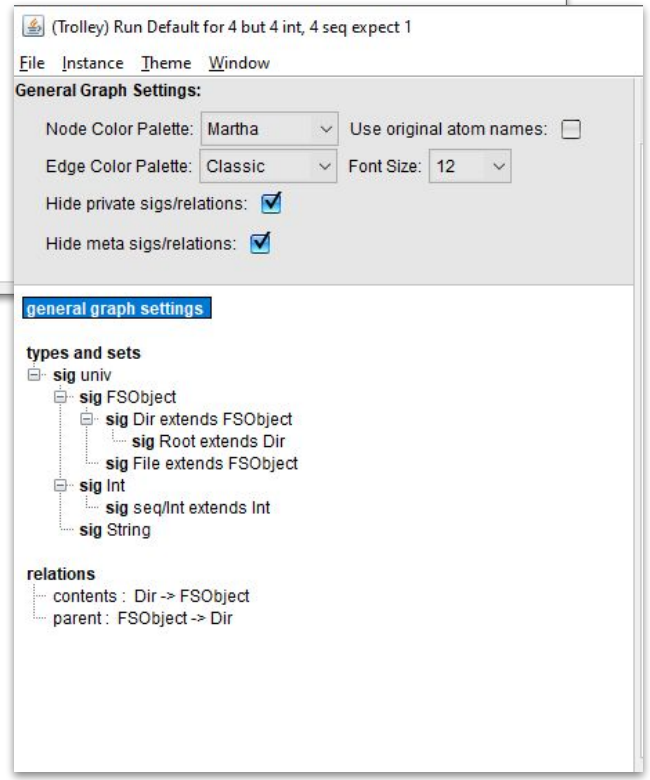


Alloy Analyzer









Extensions

Extensions

- **Alloy***
- **Electrum**
- **Aluminum**

https://groups.csail.mit.edu/sdg/pubs/2019/alloy-cacm-18-feb-22-2019.pdf

Conclusion